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1 Inventor: Dan G. Custer et al.

2 Title: Polishing Systems, Methods of Polishing Substrates, and Methods
3 of Preparing Liquids for Semiconductor Fabrication Processes

4 Assignee: Micron Technology, Inc.

5 Serial No.: 09/298,160

6 Filing Date: April 22, 1999



7 **INFORMATION DISCLOSURE STATEMENT**

8 In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is
9 directed to the United States patents listed on the attached Form PTO-1449.

10 The listed patents were cited by the Office in an application related to
11 the above-identified application. Copies of the references are enclosed.

12 Citation of these references is respectfully requested.

13 Respectfully submitted,

14
15 Dated: 5/11/99

16 Attorney: David G. Latwesen

17 David G. Latwesen, Ph.D.
18 Reg. No.: 38,533

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PTO/SB/21 (12-97)
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/298,160	
	Filing Date	April 22, 1999	
	First Named Inventor	Dan G. Custer	
	Group Art Unit	Unknown	
	Examiner Name	Unknown	
Total Number of Pages in This Submission	52	Attorney Docket Number	MI22-1172

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN David G. Latwesen, Ph.D.
Signature	
Date	May 11, 1999

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 5/11/99	
Typed or printed name	Karen S. Brasefield
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Date	May 11, 1999

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